

DEQNA ETHERNET User's Guide

Errata Sheet

Please make the following changes to the
DEQNA ETHERNET User's Guide (Document
No. EK-DEQNA-UG-001).

Page	Change
CHAPTER ONE - Introduction	
1-5	Figure 1-4. Remove the statement: "FILTER ADAPTER ASSEMBLY."
1-9	Second paragraph of Section 1.3.3.1. The first sentence should read: "The BD ROM contains minimum 'good citizen' diagnostic code for the DEQNA."
1-9	After the last paragraph of Section 1.3.3.1, insert the following: "The BD ROM also contains a DECnet secondary loader in its PDP-11 code. This includes some DECnet Maintenance Operation Protocol (MOP) code that is used to assist the following functions (see paragraph 3.2.1.): 1. ID Message 2. Loopback Assistance 3. Down-Line load."
1-11	To the third paragraph of Section 1.3.6, add the following: "Since transmit status is returned to the Q-Bus side of the DEQNA module through the receive FIFO, the receive FIFO should be empty (or that transmit status is not written until the receive FIFO is empty). Also, if any receive data is in the receive FIFO at the time a transmit status is posted, the USING FLAG will not be written for that transmit (a new receive or one that was partially DMAed and then run out of buffers). If a receive comes in after the transmit has been prefilled into the DEQNA, than the FLAG word by definition has been written."
1-14	To Table 1-1, Operating Environment, change the words "Wet Bolt" to read "Wet Bulb."

CHAPTER TWO - Installation

- 2-2 To Table 2-1, Bulkhead Cable Assembly section, change the designation of the 10-foot cable to read as follows: "CK-DEQNA-K1."
- 2-2 Add to Table 2-1, a fifth cable (now available):
"91.44 cm (36 in) shielded cable/bulkhead,
CK-DEQNA-KF."
- 2-2 To Table 2-1, remove the last two lines beginning with
"DEQNA PDP-11".
- 2-4 In section 2.3.3, the second sentence should read "two screws," not "four screws."
- 2-4 Section 2.4, step 2, should read: "the two screws saved," not "the four screws saved."
- 2-7 Reverse the order of steps 5 and 6.

CHAPTER THREE - Service

- 3-1 In section 3.1, remove the fourth unit mentioned:
"4. Ampere filter (if used)."
- 3-1 Add to section 3.2, the following table:

TABLE 3-1 DEQNA PDP-11 Diagnostic Set

Part Description	Designation
DEQNA DEC/X11 Module	CXQNAxx AC-T616x-MC
DEQNA Field Functional	CZQNAxx AC-T614x-MC
DEQNA NI Exerciser Diagnostic	CVNIAxx AC-T858x-MC

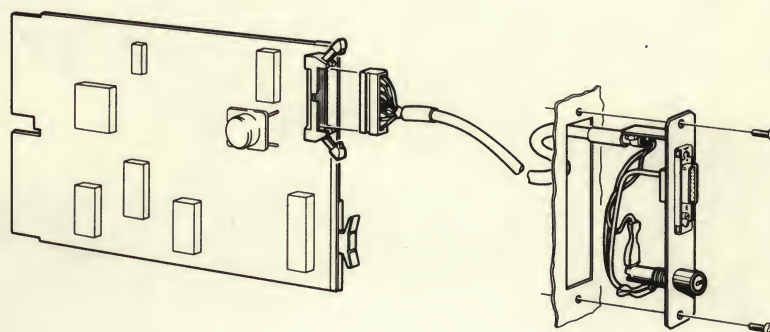
- 3-1 To section 3.2.1, add the following sentence to complete the paragraph as follows: "Also see the DEQNA PDP-11 ROM Resident Code listing referenced in 3.2."
- 3-2 Add to step 7 the following: "(Refer to paragraph 3.2.2)."
- 3-2 Add to step 8 the following: "(Refer to paragraph 3.2.2)."

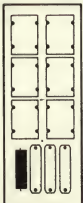
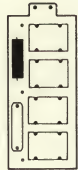



- 3-13 Remove from the first question, the following:
"IS SANITY TIMER JUMPER ENABLED/CUT (L)?
The response is Y (yes) if the sanity jumper is removed; otherwise remove the jumper and then type Y."
- 3-13 In the second question, remove the following:
"EXECUTE TESTS IN INTERNAL/"
- 3-13 Insert this fourth question before Table 3-2.
"The fourth question is:
IS LOOPBACK CONNECTOR IN DEQNA (L)?
The response is Y (yes) if a loopback connector is installed somewhere in the transceiver cable path; and N otherwise."

CHAPTER FOUR - Programming

- 4-4 The last paragraph of "EL - External Loopback", should read as follows: "This bit is cleared by power-up initialization and software reset. See BD bit also. It has no effect on set-up mode. (See Chapter one, 1.3.4)."
- 4-4 The second sentence of "IL - Internal Loopback" should read as follows: "When asserted low, it puts the DEQNA into internal loopback mode."
- 4-4 The first sentence of the second paragraph of "IL - Internal Loopback" should read as follows: "This bit is asserted low by power-up initialization and software reset to prevent a failed DEQNA from transmitting on the ETHERNET."
- 4-5 The first complete sentence of "BD - Boot/Diagnostic ROM", should read as follows: "When set, this bit causes one copy of the boot/diagnostic ROM to be loaded into the receive FIFO for subsequent transfer to host memory and bit 09 (EL) must be set for this function to succeed."
- 4-5 The third and fourth sentence of "BD - Boot/Diagnostic ROM", should read as follows: "This bit must be asserted for at least 100 microseconds and then cleared. If the operation fails (by timing out after 100 microseconds), the DEQNA is known to be bad."

- 4-5 Second paragraph, second sentence of "BD - Boot/Diagnostic ROM" should read as follows: "Because explicit receive status is not reported after the BD ROM data is transferred, and no interrupt is requested, the length of the receive buffers should be exactly 4094 bytes (the last word of all ones is not used). So that, completion is then indicated by the USED status in the final buffer's descriptor flags. (See Paragraph 4.3.2.1.)."
- 4-5 Add to the end of "NI - Non existent Memory Interrupt", the following: "(See paragraph 3.2.1.)."
- 4-9 The second complete sentence of "ABORT", the word "value" is misspelled.
- 4-9 In "ERROR/USED", change "DEST ADR (power-up" to read "DEST ADR (=power-up".
- 4-9 In "DISCARD", the first sentence should read as follows: "When set, indicates that OVF (bit 00) and CRCERR (bit 01) are valid (that is, they pertain to this packet)."
- 4-10 The third sentence of "RBL <07:00>" should read as follows: "Software must add 60 to this value, to account for the period during which address filtering occurs."



ENCLOSURE	SLOT LOCATION	CABLE LENGTH	CABLE KIT
BA123		12"	CKDEQNA - KB
BA23		12"	CKDEQNA - KB
11/23S		21"	CKDEQNA - KA
11/23+		30"	CKDEQNA - KC
1730C		36"	CKDEQNA-KF

MR-15106

Figure 4-1 DEQNA Ethernet Connections

